APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date::	09/11/03
Application Type::	Regular
Subject Matter::	Utility
Title::	Variable Latency Buffer and Method of Operation
Attorney Docket Number::	X-1214 US
Request for Early Pub?::	No
Request for Non-Pub?::	Yes
Total Drawing Sheets::	11
Small Entity?::	No
Petition included?::	No

APPLICANT INFORMATION

Applicant Authority Type::	Inventor
Primary Citizenship Ctry::	US
Status::	Full Capacity
Given Name::	Warren
Middle Name::	E.
Family Name::	Cory
Street::	538 Bay Road
City::	Redwood City
State or Province::	CA
Postal or Zip Code::	94063

Applicant Authority Type::	Inventor
Primary Citizenship Ctry::	US
Status::	Full Capacity
Given Name::	Joseph
Middle Name::	Neil
Family Name::	Kryzak
Street::	3020 Northridge Drive
City::	Ames

State or Province::   Iowa

Postal or Zip Code::   50014

CORRESPONDENCE INFORMATION

Correspondence Customer Number::   24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	

ASSIGNEE INFORMATION

Assignee Name::   Xilinx, Inc.

Street::   2100 Logic Drive

City::   San Jose

State or Province::   California

Postal or Zip Code::   95124